

Flyback Transformers For National Semiconductor LM5070 PoE Interface – 13 Watt



- Flyback transformers for 13 W PoE applications
- · Designed to operate in continuous mode at 250 kHz with an input of 36 - 72 Vdc
- 1500 Vrms isolation from primary and bias to secondary

Core material Ferrite

Terminations RoHS tin-silver over tin over nickel over phos bronze. Other terminations available at additional cost.

Weight 6.15 g

Ambient temperature -40°C to +85°C

Storage temperature Component: -40°C to +85°C.

Packaging: -40°C to +80°C

Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

Failures in Time (FIT) / Mean Time Between Failures (MTBF) 38 per billion hours / 26,315,789 hours, calculated per Telcordia SR-332

Packaging 175 per 13" reel Plastic tape: 32 mm wide, 0.5 mm thick, 28 mm pocket spacing, 12.93 mm pocket depth

PCB washing Only pure water or alcohol recommended

| Part | Inductance at 0 A ² | Inductance at Ipk ³ | DCR max (Ohms)4 | | | Leakage inductance | Turns | ratio ⁶ | Ipk ³ | |
|---------------------|-----------------------------------|-----------------------------------|-----------------|-------|-------|--------------------|---------|--------------------|------------------|---------------------|
| number ¹ | ±10% (µH) | min (μH) | pri | bias | sec | max (µH)⁵ | pri:sec | pri : bias | (A) | Output ⁷ |
| C1495-AL_ | 127 | 114.3 | 0.255 | 0.310 | 0.024 | 1.25 | 1:0.166 | 1:0.50 | 1.0 | 3.3 V, 4.0 A |
| C1585-AL_ | 127 | 114.3 | 0.222 | 0.348 | 0.039 | 0.950 | 1:0.25 | 1:0.50 | 1.0 | 5.0 V, 2.6 A |
| C1586-AL_ | 127 | 114.3 | 0.199 | 0.308 | 0.129 | 0.650 | 1:0.50 | 1:0.50 | 1.0 | 12 V, 1.08 A |

1. When ordering, please specify packaging code:

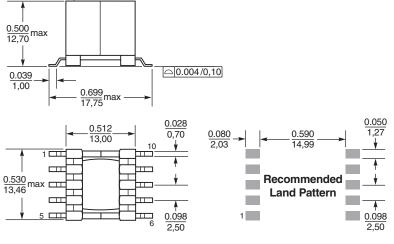
Dot above pin1

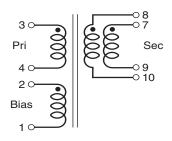
C1586-AL D

Packaging: D = 13" machine-ready reel. EIA-481 embossed plastic tape (175 parts per full reel).

- **B** = Less than full reel. In tape, but not machine ready. To have a leader and trailer added (\$25 charge), use code letter D instead.
- 2. Inductance is for the primary, measured at 250 kHz, 0.2 Vrms, 0 Adc.
- 3. Ipk is peak primary current drawn at minimum input voltage.
- 4. DCR for the secondary is per winding.
- 5. Leakage inductance measured between pins 3 and 4 with all other pins shorted.
- 6. Turns ratio is with the secondary windings connected in parallel.
- 7. Output of the secondary is with the windings connected in parallel. Bias winding output is 12 V, 20 mA.
- 8. Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.





Secondary windings to be connected in parallel on the PC board

Specifications subject to change without notice. Please check our website for latest information.

Document 445 Revised 10/29/08